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FNV2R33C5

7 SEGMENT DISPLAY + DECIMAL POINT, BCD INPUTS 24 V (**PNP**)
FRONT MOUNTING, MODUL WIDTH 16 MM
DIGIT: HEIGT 13 MM, COLOR: RED
OUTPUT: TIN-PLATED PINS
INTERNAL LOGIC: C-MOS - POWER: 24 V CC

High-level 24 V positive logic input, as from programmable controllers, type PNP, with decimal decoder, memory (input EL), segment test (input LT), and display off (input BI).

TECHNICAL DATA

Digit: Heigt		13	mm
Supply Voltage	V_{al}	18 — 30	V
Supply Current at V_{al} = 24 V	I_{al}	40	mA
Max Supply Voltage	V_{in} max	V_{al} + 0,5	V
Input Voltage at low level	V_{inl}	-3,5 — 4	V
Input Voltage at high level	V_{inh}	11 — V_{al} + 0,5	V
Input Current at V_{in} = 0	I_{inl}	-5	μA
Input Current at V_{in} = 24 V (V_{in} / 10 KΩ)	I_{inh}	2,5	mA
Input Current decimal point at V_{in} = 24 V	I_{dp}	5	mA

TABLE OF TRUTH

EL	BI	LT	8	4	2	1	DISPLAY
X	X	L	X	X	X	X	**8**
X	L	H	X	X	X	X	OFF
L	H	H	L	L	L	L	**0**
L	H	H	L	L	L	H	**1**
L	H	H	L	L	H	L	**2**
L	H	H	L	L	H	H	**3**
L	H	H	L	H	L	L	**4**
L	H	H	L	H	L	H	**5**
L	H	H	L	H	H	L	**6**
L	H	H	L	H	H	H	**7**
L	H	H	H	L	L	L	**8**
L	H	H	H	L	L	H	**9**
L	H	H	H	L	H	L	OFF
L	H	H	H	L	H	H	OFF
L	H	H	H	H	L	L	OFF
L	H	H	H	H	L	H	OFF
L	H	H	H	H	H	L	OFF
L	H	H	H	H	H	H	OFF
H	H	H	X	X	X	X	STORED[1)]

ATTENZIONE !!

Inputs are polarized
to LOW level (R=~10 KΩ)
i.e.:
no input = low level.

The standard operation is obtained with LT and BI at **HIGH level**.

1) The data is stored during the transition L → H of EL input.
H=high level X=indifferente L=livello basso

NB: In a group of 2 or more digits Enable Latch is used to control several digits with the same flat cable.

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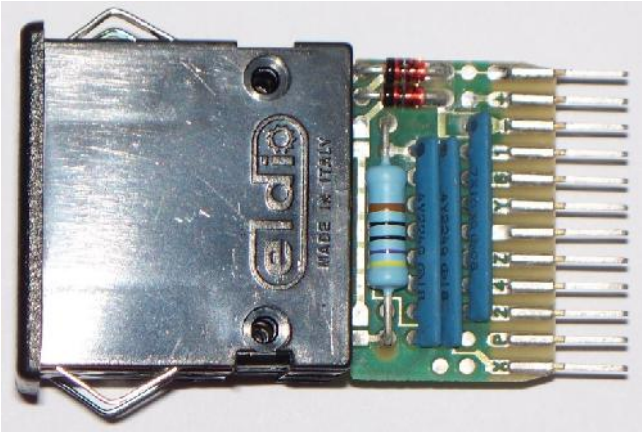
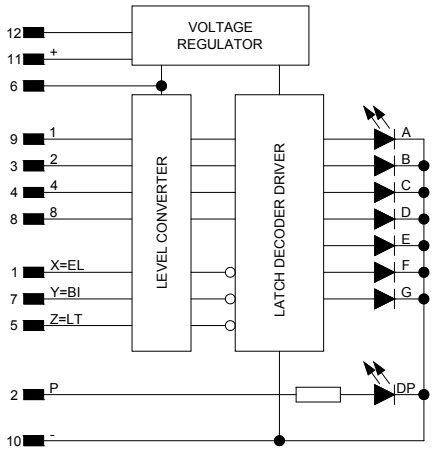
COLLEGAMENTI



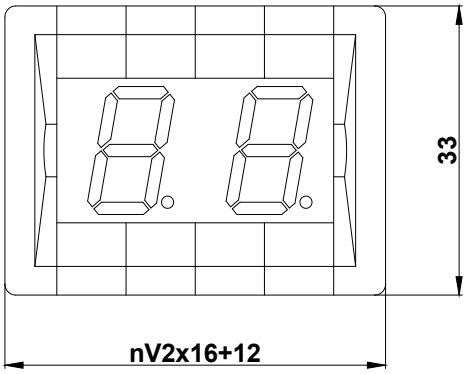
pin 12		DO NOT CONNECT !! (internal connection)
pin 11	(+)	INPUT 24 V
pin 10	(-)	0 V
pin 9	(1)	DATA INPUT BCD "1"
pin 8	(8)	DATA INPUT BCD "8"
pin 7	(Y)	BLANKING (L = all segments off)
pin 6		V internal logic (for decimal point)
pin 5	(Z)	LAMP TEST (all segments on)
pin 4	(4)	DATA INPUT BCD "4"
pin 3	(2)	DATA INPUT BCD "2"
pin 2	(P)	DECIMAL POINT (to be connected to pin n° 6 to turn on)
pin 1	(X)	ENABLE LATCH (Low = trasparent ; High = store)

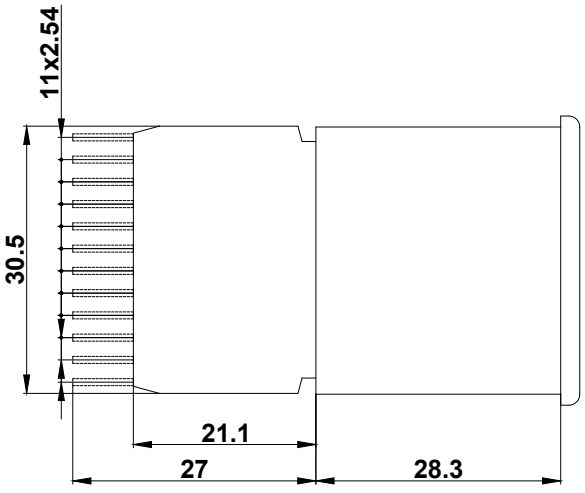
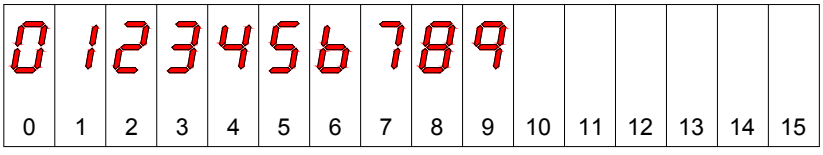
N.B.: With the display in standard position (decimal point lower right), the pin no. 1 is the lowest, the pin no. 12 is the highest.

ELECTRIC DIAGRAM



OVERALL DIMENSIONS





DISPLAY FORMAT